

SYNCHRONISER

Grant agreement no.: 248867

Synchroniser Steering Committee (SSC) First Think Tank Meeting

30th September 2010

at

HOTEL AQUA

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Participants

SYNCHRONISER Steering Committee Members

Name	Organization	Country
Mr. Albert Sitja	Catalonian Technological Telecommunications Center	Spain
Mr. Latif Ladid	IPv6 Forum	Luxembourg
Mr. Laurent Thomasson	EADS ASTRIUM	France
Mr. Stuart Campbell	TIE	UK
Mr. Laurent Schwab	Reseau Asie – Fondation Maison des Sciences de l'Homme	France
Dr A K Chakravarti	C-DAC	India
Dr Hiranmay Ghosh	TCS Innovation Labs	India
Dr Mukul K Sinha	Expert Software Consultants Ltd	India
Mr Vinnie Mehta	Automotive Components Manufacturers' Association (ACMA)	India

Project Partners

Organisation	Country	Participants
TESEO - coordinator	Belgium	Ms Katja Legisa Mr Mauro Bianchi
EIRC Pvt Ltd	India	Ms Surbhi Sharma
C-DAC	India	Mr Mahesh Kulkarni Mr Jasjit Singh
ROSE Vision	Spain	Ms Sara Munoz
STPI	India	Ms Vandana Srivastava
IISc	India	Dr Mary Mathew
FhG	Germany	Mr Kay Matzner

AGENDA: Thursday, 30th of September 2010	
Time	Agenda
09:15 - 09:30	Arrival of participants and welcome tea/coffee
09:30 - 09:45	Welcome and introduction of the working day - ROSE <ul style="list-style-type: none"> ▪ Brief introduction of the working day ▪ Introduction of participants
09:45 - 10:30	Role of the SSC w.r.t. EU-India ICT cooperation - ROSE/CDAC <ul style="list-style-type: none"> ▪ Significance of the Think Tank meetings and their expected outcomes ▪ Roadmap for the ensuing period ▪ Answers to questions that the SSC members may have
10:30 - 10.45	Presentation on Position Paper for the 7th JWG Meeting - STPI
10:45 - 11:00	Coffee break
11:00 - 13:00	Brainstorming session on objectives of the project - ROSE/CDAC/SSC members <ul style="list-style-type: none"> ▪ Specific questions on EU-Indian policy dialogue ▪ Analysis of the 6th JWG Minutes of Meeting ▪ Concordance on priority areas of ICT cooperation for collaborative research and development ▪ Opportunity of creating an EU-India joint corpus for funding schemes in these areas. Existing financing models for EU-India R&D vs. possible new models/scenarios
13:00 - 14:00	Lunch

AGENDA: Thursday, 30th of September 2010	
Time	Agenda
14:00 - 15:30	<p>Brainstorming session continues - ROSE/CDAC/SSC members</p> <ul style="list-style-type: none"> ▪ Policy framework for: <ul style="list-style-type: none"> ○ Evaluation of research proposals & assessment of project progress ○ Utilization of research outcomes through shared IPR and Transfer of Technology to industry on both sides ▪ Consolidation of conclusions to evolve an effective proposal that can be submitted to the EU-India JWG
15.30 - 16.00	<p>Summary and conclusions - ROSE/CDAC</p> <ul style="list-style-type: none"> ▪ Action plan with dates and action points for ensuing period before the JWG meeting